

115-02
115-02
501.40724X01
PRO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

09/974814
U.S. PRO
10/12/01

Applicant(s) : SAHARA et al.

Serial No. : Not yet assigned

Filed: On even date

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND THE PROCESS OF THE SAME

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98**

Assistant Commissioner
for Patents
Washington, D.C. 20231

October 12, 2001

Sir:

In the matter of the above-identified application,
applicants are submitting herewith copies of the documents
listed in the attached form equivalent to Form PTO-1449 for
the Examiner's consideration.

This information disclosure statement is being submitted
with the new application.

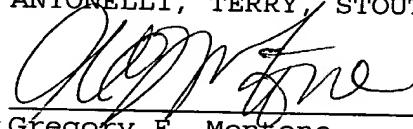
To the extent the documents listed on the attached form
equivalent to Form PTO-1449 are not in the English language,
the requirement of 37 CFR 1.98(a)(3) for a concise explanation
of the relevance is satisfied by an English language version
of the documents and the discussion of these documents in the
specification, for example, on pages 2 and 3.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (501.40724X00) please credit any excess fees to such deposit account.

Respectfully submitted,

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